

PATENTS
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IN THE CLAIMS

1-3. (Canceled)

4. (Currently amended) A system for processing signals, comprising:

a multiplexer, the multiplexer having a first interface to a plurality of broadband signal inputs and a second interface to a bus, the multiplexer multiplexing signals received at each of the broadband signal inputs onto the bus according to a frequency allocation scheme that associates each of the broadband signal inputs with an assigned frequency block;

a plurality of receivers in communication with the bus for receiving and decoding the multiplexed broadband signal inputs for communication with a data network; wherein each of the receivers is tunable ~~capable of tuning~~ to a frequency corresponding to a frequency block of the frequency allocation scheme;

wherein at least one of the receivers is a backup receiver capable of being activated by an activation control signal that is generated upon detection of a fault condition; and

wherein activation of a backup receiver comprises tuning the backup receiver to a frequency corresponding to a failed receiver.

5-22. (Canceled)